

NAS 2024

Guangzhou, China

Chimlong International Conference Center

The 17th International Conference On Networking, Architecture, and Storage

<http://nas-conf.org> | Nov.29 – Dec.1, 2024



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The International Conference on Networking, Architecture, and Storage provides a high-quality international forum to bring together researchers and practitioners from academia and industry to discuss cutting-edge research on networking, high-performance computer architecture, and parallel and distributed data storage technologies. NAS 2024 will expose participants to the most recent developments in the interdisciplinary areas.

Each submission can be either a regular paper (up to 8 pages) or a short paper (up to 4 pages), including all text, figures, tables, footnotes, appendices, references, etc. Authors are invited to submit previously unpublished work for possible presentation at the conference. The program committee will nominate best papers for recognition in the three conference topic areas. All papers will be evaluated based on their novelty, fundamental insight, experimental evaluation, and potential for long-term impact; new idea papers are encouraged. All accepted papers will be published in IEEE digital library. NAS 2024 is technically sponsored by IEEE TCDP.

- Accelerator-based architectures
- Ad hoc and sensor networks
- Application-specific, reconfigurable or embedded architectures
- Architecture for handheld or mobile devices
- Architecture, networking or storage modeling and simulation methodologies
- Big Data infrastructure
- Big Data services and analytics
- Cloud and grid computing
- Cloud storage
- Data-center scale architectures
- Effects of circuits and emerging technology on architecture
- Energy-aware storage
- File systems, object-based storage
- GPU architecture and programming
- HW/SW co-design and trade-offs
- Mobile and wireless networks
- Network applications and services
- Network architecture and protocols
- Network information theory
- Network modeling and measurement
- Network security
- Non-volatile memory technologies
- Parallel and multi-core architectures
- Parallel I/O
- Power and energy efficient architectures and techniques
- Processor, cache, memory system architectures
- Software defined networking
- Software defined storage
- SSD architecture and applications
- Storage management
- Storage performance and scalability
- Storage virtualization and security
- Virtual and overlay networks

Important Dates:

Paper submission deadline:

~~July 31, 2024~~ **August 11, 2024 (AOE)**

Author notification:

September 15, 2024

Camera-ready submission:

October 6, 2024