



NAS 2021 Call for Papers



The 15th IEEE International Conference on
Networking, Architecture and Storage (NAS 2021, sponsored by the IEEE)



Riverside Convention Center, CA, October 24-26, 2021, <http://www.nas-conference.org>

General Chair:

Laxmi N. Bhuyan, University of California, Riverside

Program Co-Chairs:

Chita Das, Penn State University
Qing Yang, University of Rhode Island
Dipak Ghosal, University of California, Davis
Hong Jiang, University of Texas, Arlington

Vice Program Chairs:

Networking:

Yan Luo, UMass Lowell

Architecture:

E.J. Kim, Texas A&M

Storage:

Yifeng Zhu, University of Maine

Industry panel/track chair:

Asit Mishra, NVIDIA

Local Arrangement Chair:

John Dung Vu, California State University, San Bernardino

Finance Chair

John Zhiyong Xu, Suffolk University

Registration Co-Chairs:

Yun Tian: California State University, Fullerton

Xiaorong Zhang, San Francisco State University

Industry Sponsorship co-chairs:

Ravi Iyer, Intel

Changsheng Xie, Huazhong University of Sc. and Tech., China

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The International Conference on Networking, Architecture, and Storage provides a high-quality international forum to bring together researchers and practitioners from academia and industry to discuss cutting-edge research on networking, high-performance computer architecture, and parallel and distributed data storage technologies. NAS 2019 will expose participants to the most recent developments in the interdisciplinary areas.

Each submission can be either a regular paper (up to 8 pages) or a short paper (up to 4 pages), including all text, figures, tables, footnotes, appendices, references, etc. Authors are invited to submit previously unpublished work for possible presentation at the conference. The program committee will nominate best papers for recognition in the three conference topic areas. All papers will be evaluated based on their novelty, fundamental insight, experimental evaluation, and potential for long-term impact; new-idea papers are encouraged. All accepted papers will be published in IEEE digital library. Selected and extended papers will be recommended for journal publications.

- Accelerator-based architectures
- Ad hoc and sensor networks
- Application-specific, reconfigurable or embedded architectures
- Architecture for handheld or mobile devices
- Architecture, networking or storage modeling and simulation methodologies
- Big Data infrastructure
- Big Data services and analytics
- Cloud and grid computing
- Cloud storage
- Data-center scale architectures
- Effects of circuits and emerging technology on architecture
- Energy-aware storage
- File systems, object-based storage
- GPU architecture and programming
- HW/SW co-design and tradeoffs
- Mobile and wireless networks
- Network applications and services
- Network architecture and protocols
- Network information theory
- Network modeling and measurement
- Network security
- Non-volatile memory technologies
- Parallel and multi-core architectures
- Parallel I/O
- Power and energy efficient architectures and techniques
- Processor, cache, memory system architectures
- Software defined networking
- Software defined storage
- SSD architecture and applications
- Storage management
- Storage performance and scalability
- Storage virtualization and security
- Virtual and overlay networks

Important Dates (tentative):

Paper submission deadline: **May 30, 2021**

Author notification: **July 15, 2021**

Camera-ready submission: **August 15, 2021**