

NAS 2017 Call for Papers

The 12th International Conference on



Networking, Architecture and Storage (NAS 2017, sponsored by the IEEE)



Shenzhen, China, August 7-8, 2017, http://www.nas-conference.org/

General Chair

Cheng-Zhong Xu (SIAT, Shenzhen, China)

Vice General Chair

Zhibin Yu (SIAT, Shenzhen, China)

Program Co-Chairs:

Haiying Shen (U of Virginia) Christian Engelmann (ORNL)

Vice Program Chairs:

Networking: Rolf Riesen (Intel)

Pavel Shamis (ARM)

Architecture: Michela Becchi (NCSU)

Eun Jung (EJ) Kim (TAMU)

Storage: Yu Gu (IBM Watson)

Weijun Xiao (VCU)

Local Arrangement Chair:

Xiaopeng Fan (SIAT, Shenzhen, China)

Publication Chair:

Jia Rao (U of Texas at Arlington)

Registration & Finance Chair:

Summer Zhang (SIAT, Shenzhen, China)

Publicity Chairs:

Kang Chen (SIU, USA) Ignacio Castro (UOC, Spain) Fangming Liu (HUST, China) Husnu S. Narman (Clemson, USA)

Web Chair:

Zhuozhao Li (U of Virginia)

Steering Committee

Xubin He (Temple University)
Andre Brinkmann (U of Mainz)
Zizhong Chen (UC Riverside)
Hong Jiang (U of Texas at Arlington)
Song Jiang (U of Texas at Arlington)
Darren Kerbyson (PNNL)
Yan Luo (U of Massachusetts Low)
Jun Wang (U of Central Florida, USA)
Changsheng Xie (HUST, China)
Qing Yang (U of Rhode Island)

The International Conference on Networking, Architecture, and Storage provides a high-quality international forum to bring together researchers and practitioners from academia and industry to discuss cutting-edge research on networking, high-performance computer architecture, and parallel and distributed data storage technologies. NAS 2017 will expose participants to the most recent developments in the interdisciplinary areas.

Authors are invited to submit previously unpublished work for possible presentation at the conference. The program committee will nominate best papers for recognition in each of the three conference topic areas. All papers will be evaluated based on their novelty, fundamental insight, experimental evaluation, and potential for long-term impact; new-idea papers are encouraged. All accepted papers will be published in IEEE digital library.

Papers are solicited in fields that include, but are not limited to, the following:

- Accelerator-based architectures
- Ad hoc and sensor networks
- Application-specific, reconfigurable or embedded architectures
- Architecture for handheld or mobile devices •
- Architecture, networking or storage
 modeling and simulation
 methodologies
- Big Data infrastructure
- Big Data services and analytics
- Cloud and grid computing
- Cloud storage
- Data-center scale architectures
- Effects of circuits and emerging technology on architecture
- Energy-aware storage
- File systems, object-based storage
- GPU architecture and programming
- HW/SW co-design and tradeoffs

- Mobile and wireless networks
- Network applications and services
- Network architecture and protocols
- · Network information theory
- Network modeling and measurement
- Network security
- Non-volatile memory technologies
- Parallel and multi-core architectures
- Parallel I/O
- Power and energy efficient architectures and techniques
- Processor, cache, memory system architectures
- Software defined networking
- · Software defined storage
- SSD architecture and applications
- Storage management
- Storage performance and scalability
- Storage virtualization and security
- Virtual and overlay networks

Important Dates:

Paper Submission: Notification: Camera Ready submission: May 2, 2017 (AoE) June 5, 2017 June 26, 2017