

## Long Beach, California, USA, August 8-10, 2016 http://www.nas-conference.org/

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Authors are invited to submit previously unpublished work for possible presentation at the conference. The program committee will nominate **best papers** for recognition in each of the three conference topic areas. All papers will be evaluated based on their novelty, fundamental insight, experimental evaluation, and potential for long-term impact; new-idea papers are encouraged. All accepted papers will be published in IEEE digital library.

## Papers are solicited in fields that include, but are not limited to, the following:

- Processor, cache, memory system architectures
- Parallel and multi-core architectures
- GPU architecture and programming
- Data-center scale architectures
- Architecture for handheld or mobile devices
- Accelerator-based architectures
- Application-specific, reconfigurable or embedded architectures
- HW/SW co-design and tradeoffs
- Power and energy efficient architectures and techniques
- Effects of circuits and emerging technology on architecture
- Cloud and grid computing
- Architecture, networking or storage modeling and simulation methodologies
- Non-volatile memory technologies
- Mobile and wireless networks

## **Important Dates:**

Paper Submission: April 10, 2016 Notification: May 29, 2016 Camera-Ready Copy: June 26, 2016 Ad hoc and sensor networks

- Network security
- Network information theory
- Software defined networking
- Network applications and services
- Network architecture and protocols
- Virtual and overlay networks
- Network modeling and measurement
- Storage management
- Storage performance and scalability
- File systems, object-based storage
- Energy-aware storage
- SSD architecture and applications
- Parallel I/O
- Cloud storage
- Storage virtualization and security
- Software defined storage
- Big Data infrastructure
- Big Data services and analytics

NAS 2016 is sponsored by the IEEE Computer Society's Technical Committees on Computer Architecture (TCCA), Parallel Processing (TCPP) and Distributed Processing (TCDP).