ORGANIZING & PROGRAM COMMITTEES

Steering Committee

Xubin He, Tennessee Technological University, USA

Hong Jiang, University of Nebraska -Lincoln, USA Changsheng Xie, Huazhong Univ. of Science and Technology, China Qing Yang, University of Rhode Island, USA

Honorable General Co-Chairs

Laxmi Bhuyan, University of California,

Wei Zhao, University of Macau, Macau SAR, China

General Co-Chairs

Xiuzhen Cheng, George Washington University, USA

Yifeng Zhu, University of Maine, USA

Program Co-Chairs

Andre Brinkmann, Paderborn Center for Parallel Computing, Germany Dong Xuan, The Ohio State University,

Program Vice Chairs

Networking

Weijia Jia, City University of Hong Kong,

Qun Li, College of William and Mary, USA

<u>Architecture</u>

Jizhong Han, Chinese Academy of Sciences, China

Mario Porrmann, University of Paderborn, Germany

Storage

Gabriel Antoniu, INRIA Rennes – Bretagne Atlantique Center, France Xiao Qin, Auburn University, USA

Award Chair

Jie Wu, Temple University, USA

Publication Chair

Zhen Jiang, West Chester University of Pennsylvania, USA

Publicity Chair

Avinash Srinivasan, Bloomsburg University, USA Wei Lou, The Hong Kong Polytechnic University, China

Finance/Registration

Qiang Cao, Huazhong University of Science and Technology, China Min Song, Old Dominion University, USA

Industry Chair

Fang Liang, National University of Defense Technology, China

CALL FOR PAPERS



NAS 2010

The 5th International Conference on

Networking Architecture and Storages

Macau SAR, China July 15-17, 2010

http://www.nas-conference.org/cfp.html

The 5th IEEE International Conference on Networking, Architecture, and Storage (NAS 2010) will be held from July 15 - 17, 2010 at the University of Macau, Macau SAR, China. It will serve as an international forum to bring together researchers and practitioners from academia and industry to discuss cutting-edge research on networking, high-performance computer architecture, and data storage technologies.

SCOPE

Topics of particular interest include, but are not limited to:

Network Architectures and Protocols Virtual and Overlay Networks **Network Applications and Services** Ad hoc and Sensor Networks Mobile Handset Networks RFID Systems and Networks Network Security and Privacy **Processor Architectures** Cache and Memory Systems Parallel Computer Architectures Power Efficient Architectures

High-Availability Architectures Reconfigurable Architectures Innovative HW/SW Tradeoffs Storage Management Parallel I/O Architectures Storage Performance and Scalability File Systems, Object-based storage, and block storage Energy-aware storage Storage networking

Architecture and applications of solid state disks

PAPER SUBMISSION

Form of Manuscript: The submitted manuscript is not to exceed 10 two-column numbered pages in IEEE Xplore format. (8.5x11-inch paper, margins in inches-- top: 0.75, bottom: 1.0, sides:0.625, and between columns:0.25, main text: 10pt).

All papers submitted to this conference will be peer-reviewed and accepted on the basis of their scientific merit and relevance to the conference topics. All accepted papers will be published by IEEE CPS, which are indexed through EI.

IMPORTANT DEADLINES

Paper Submission Acceptance Notification Camera-ready copy

March 1, 2010 (23:59 EST) **April 8, 2010** May 10, 2010

For further information, please contact the program co-chairs, Prof. Andre Brinkmann at Andre.Brinkmann@uni-paderborn.de or Prof. Dong Xuan at xuan@cse.ohio-state.edu